

FIG. 1a

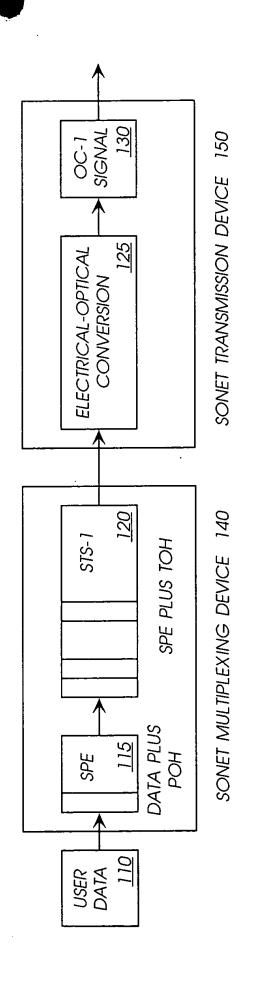


FIG. 1b

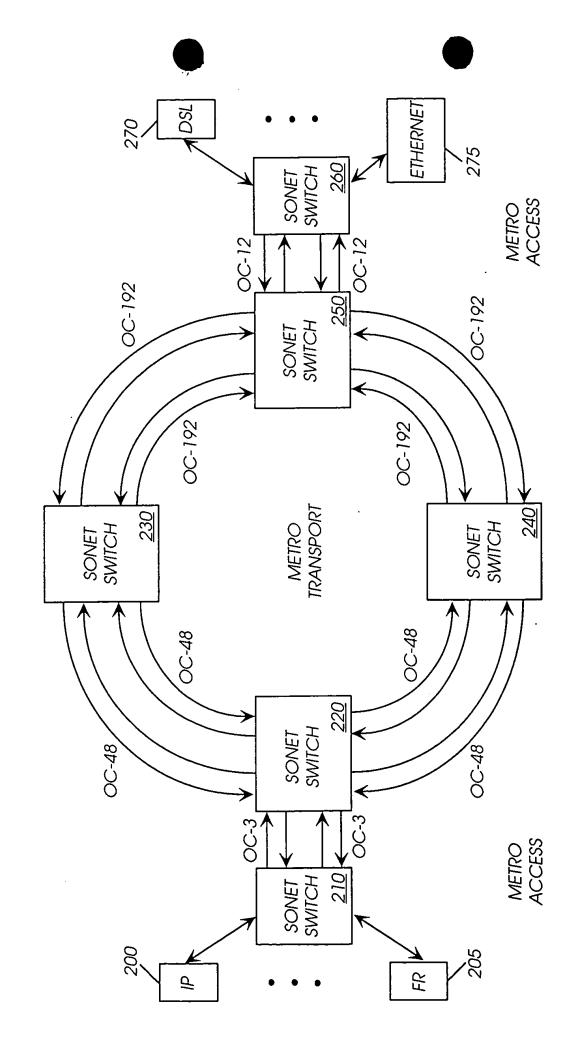


FIG. 2

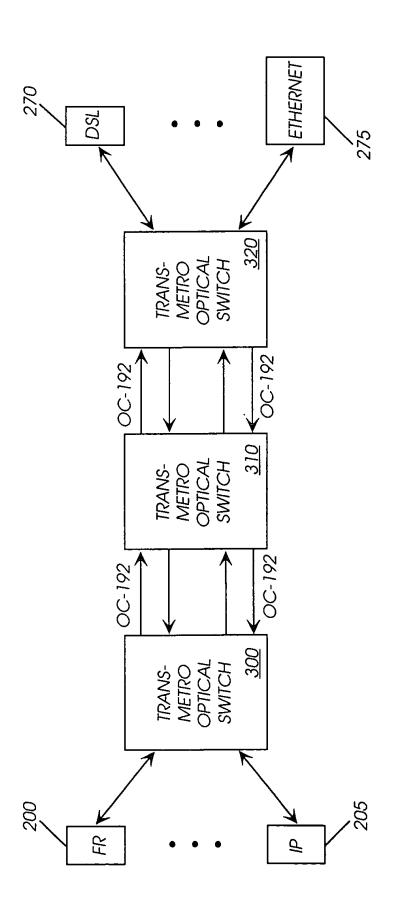
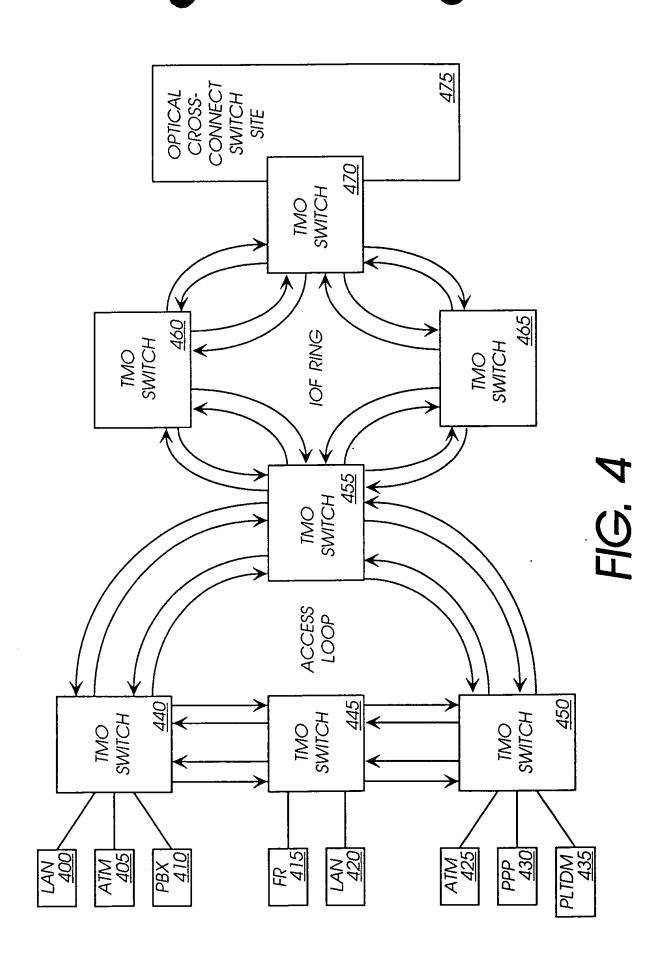


FIG. 3



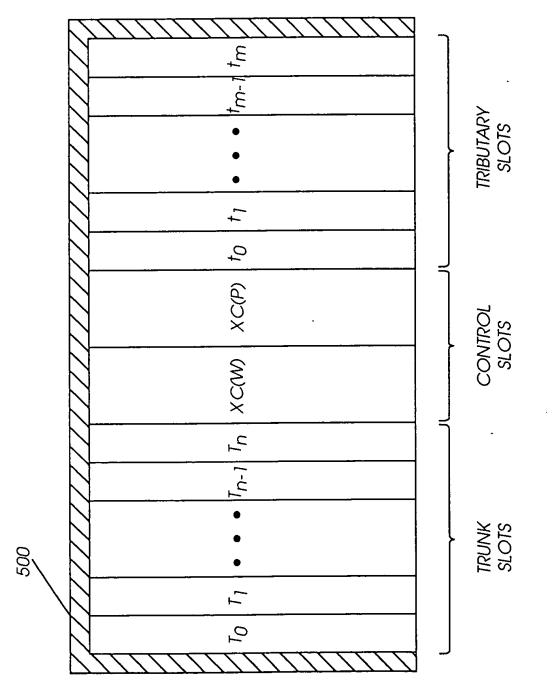
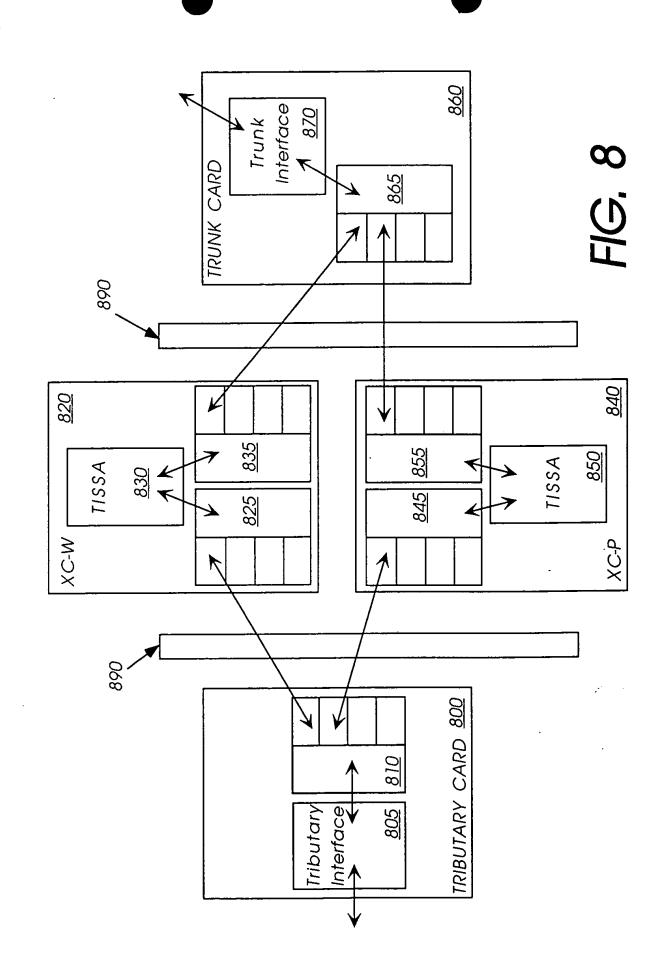


FIG. 5

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F16. 6



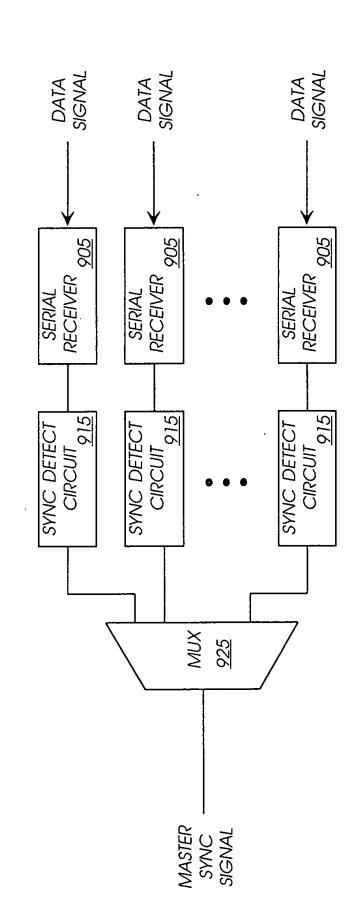
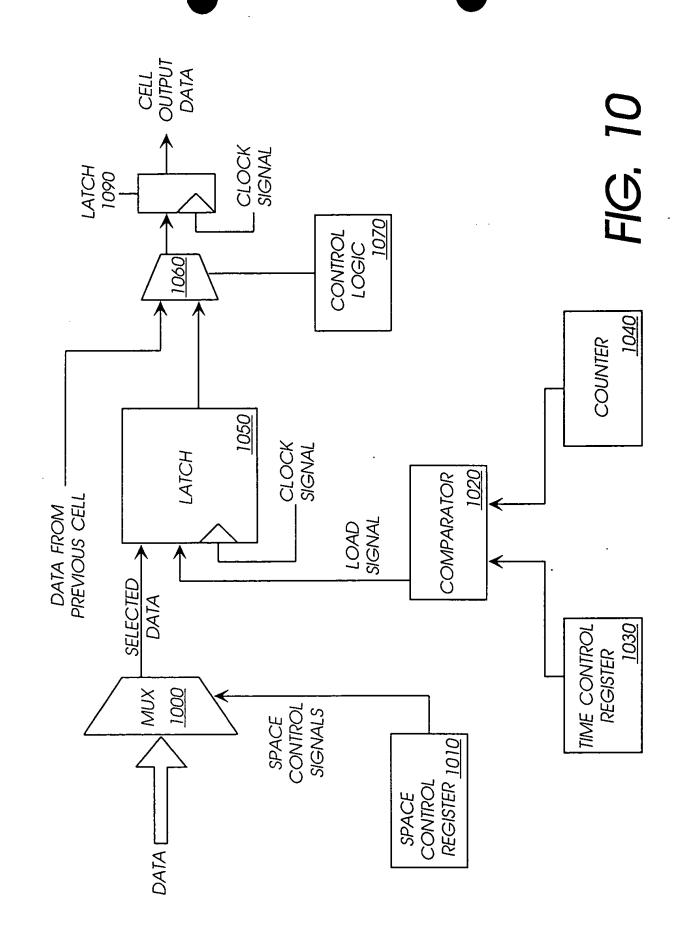
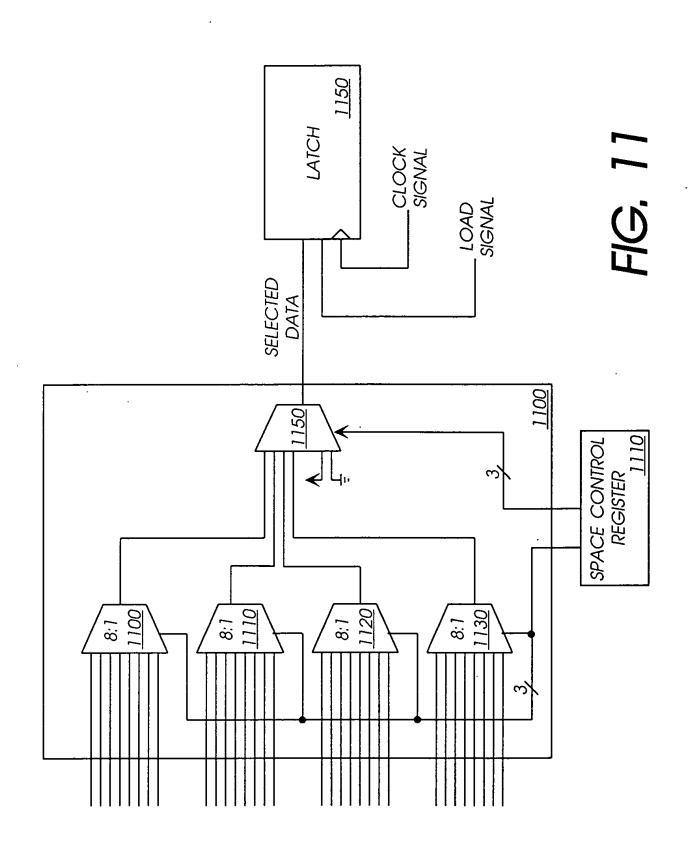
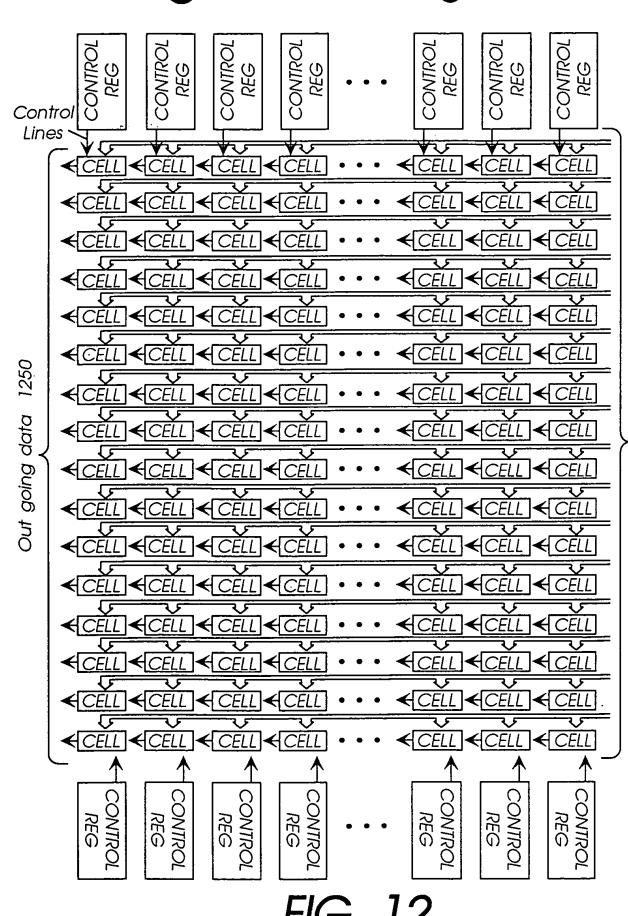


FIG. 9a

										96
	915	020	935	940	955	096	975	086	COO	FIG.
	T_X	$R_{\mathbf{X}}$	I_X	R_X	I_X	R_X	T_{X}	R_{X}		
:		<u>010</u>		<u>930</u>		<u>950</u>		026		
	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 0	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 1	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 2	PARALLEL/SERIAL CONVERSION CIRCUIT	GROUP 3	CONTROL INTERFACE	•
006	12		91	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	16		91			







1200

In coming data

FIG. 12

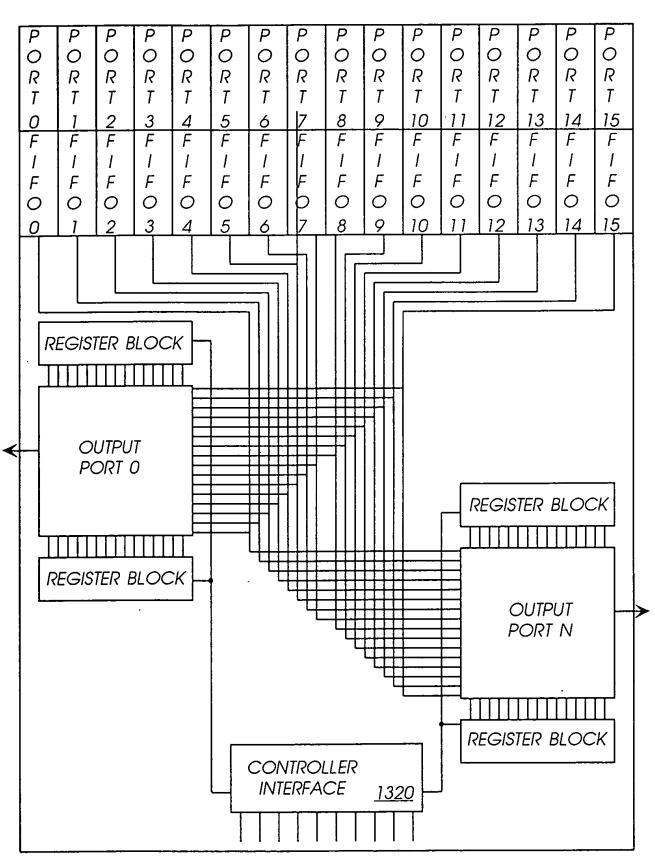


FIG. 13

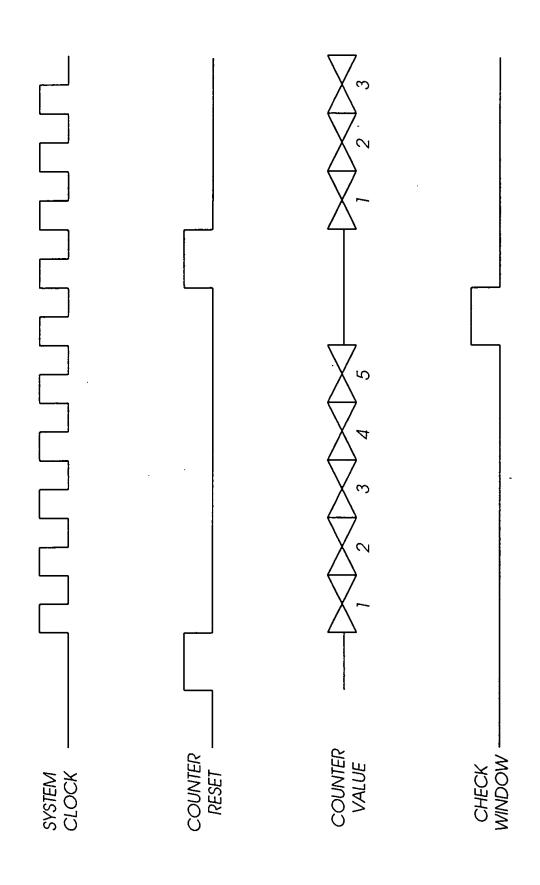


FIG. 14

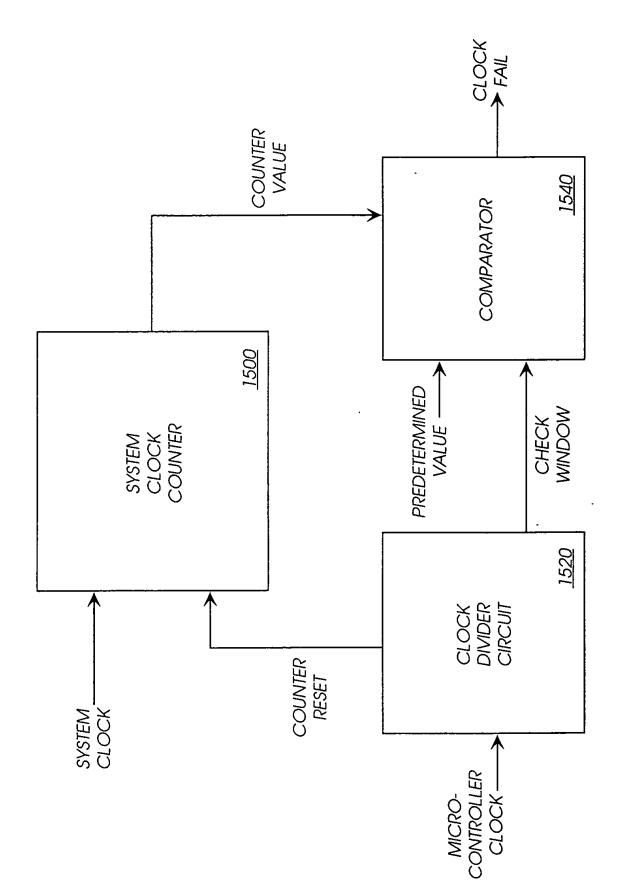


FIG. 15

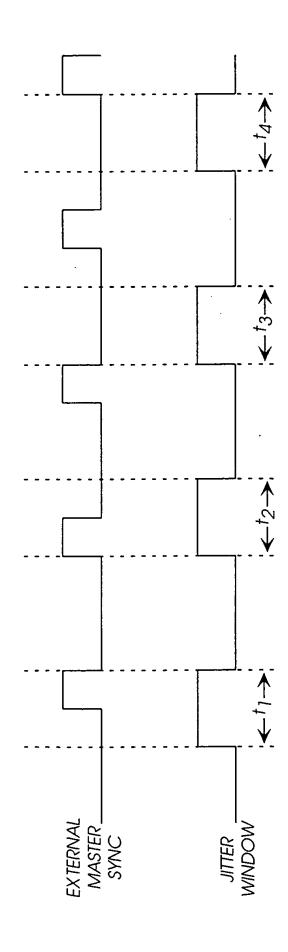
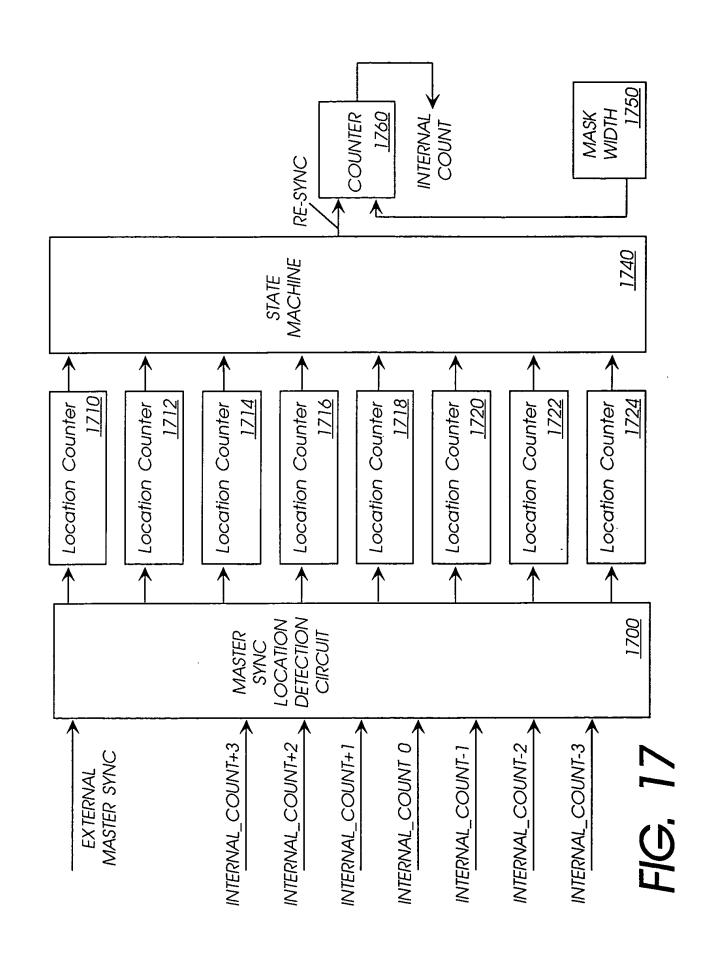


FIG. 16



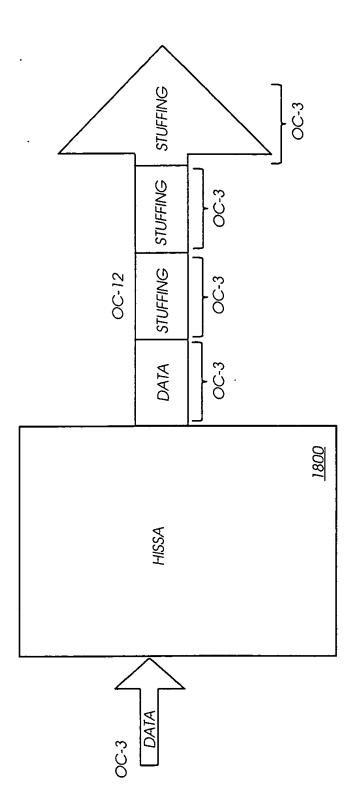


FIG. 18

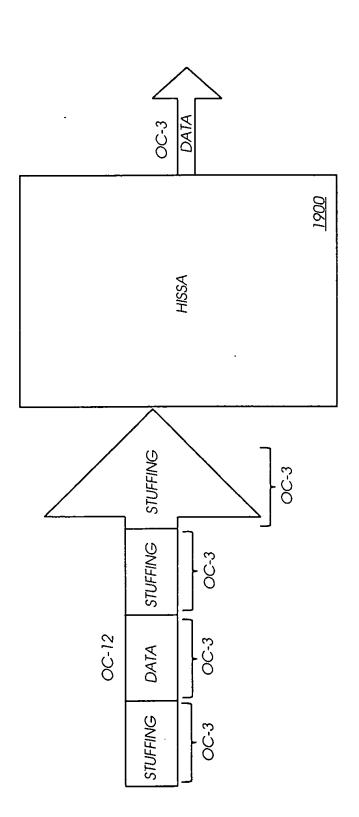
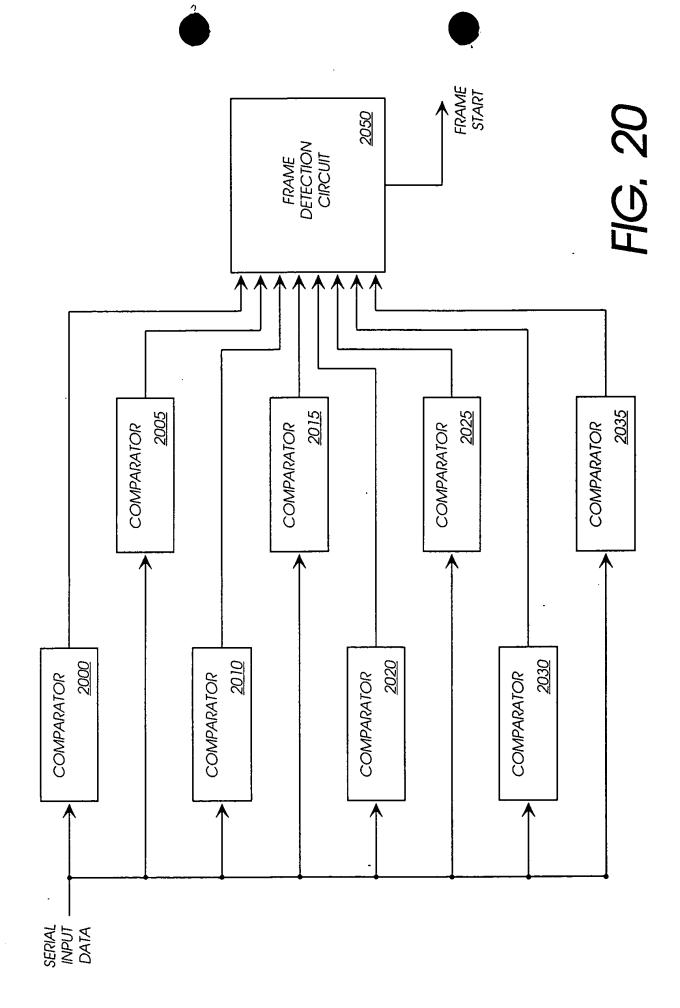


FIG. 19



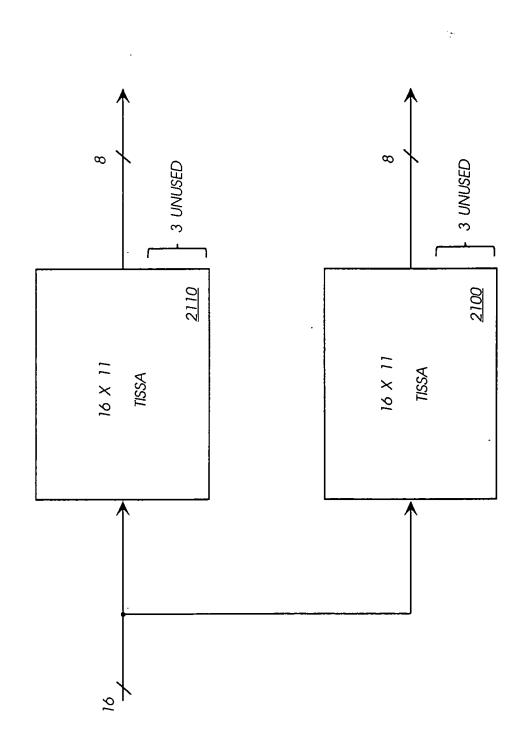
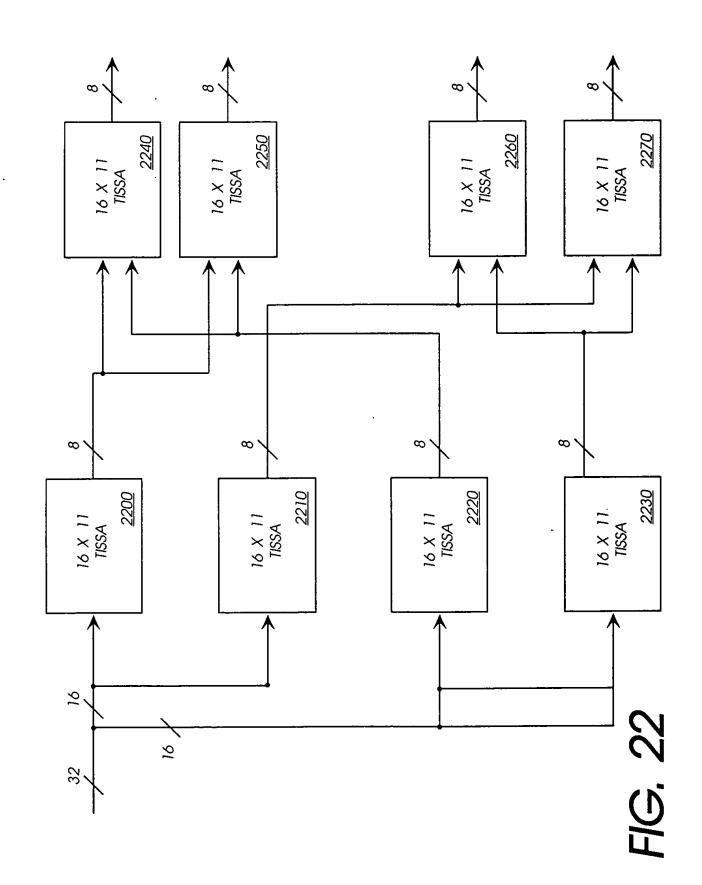


FIG. 21



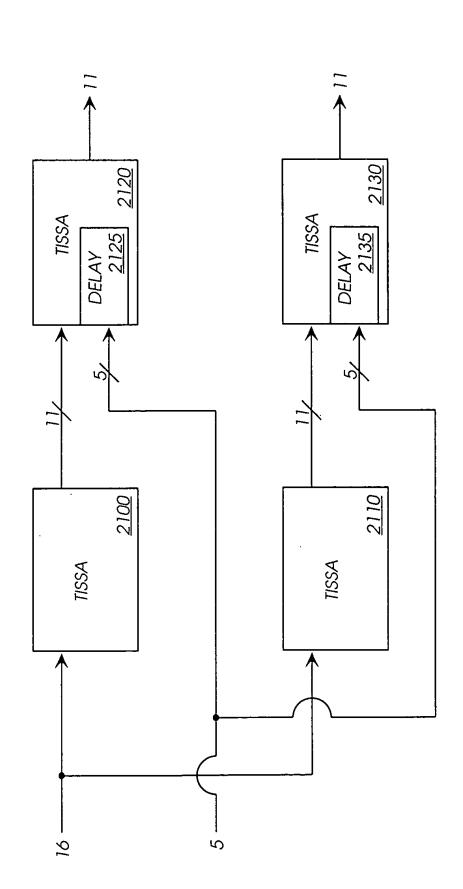


FIG. 23